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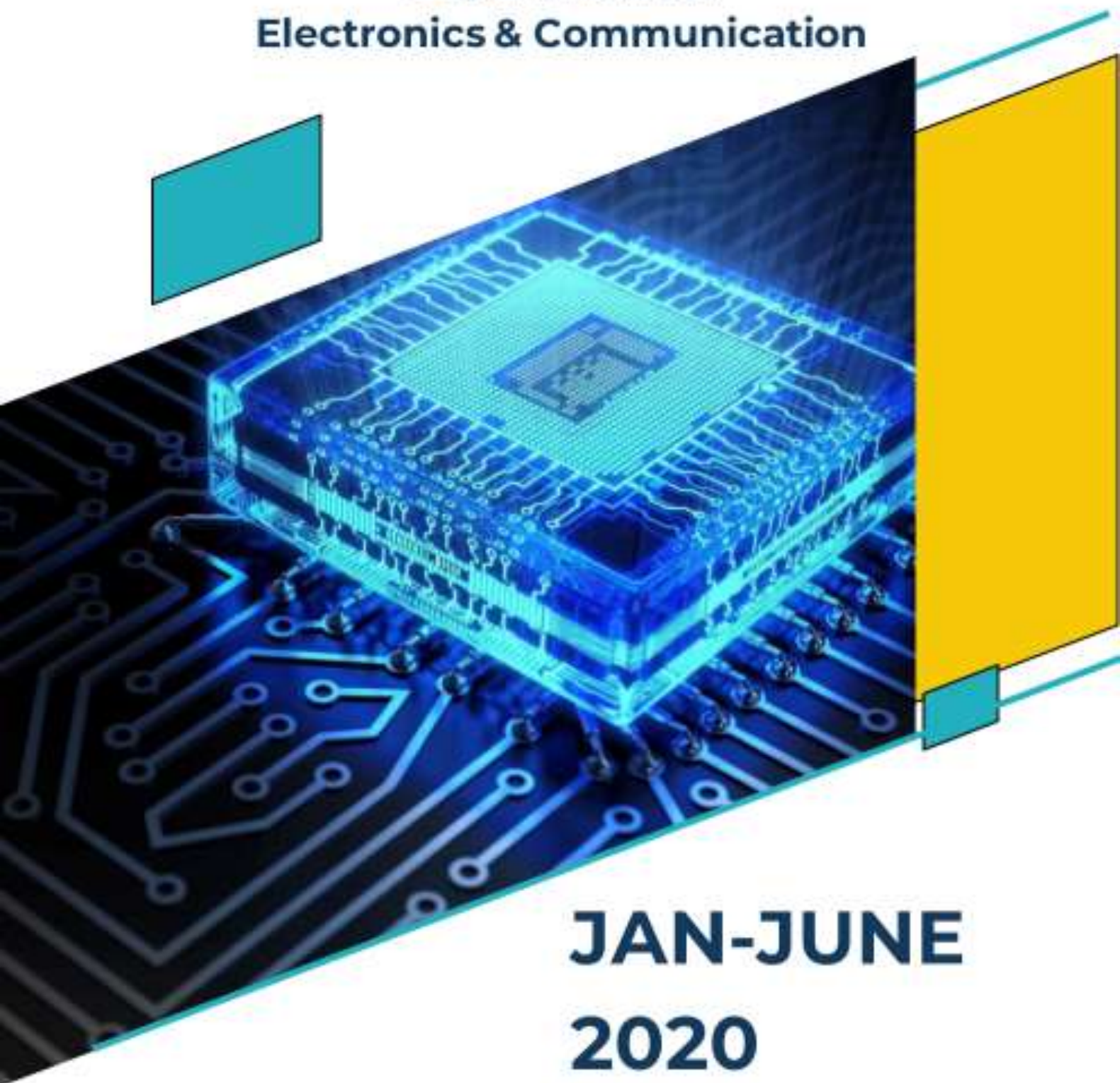
**SISTec Engg.**

GANDHI NAGAR CAMPUS | [www.sistecgn.ac.in](http://www.sistecgn.ac.in)

*Approved by AICTE & Affiliated to RGPV Bhopal*

# NEWSLETTER

Department of  
Electronics & Communication



**JAN-JUNE  
2020**

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# Editorial Message

*"No resistance can drop our potential"*

Heartily welcome to the 2<sup>nd</sup> Edition of our Departmental Newsletter.

We are very delighted and proud in presenting this newsletter to showcase the latest developments and programs which happened in our department.

It is indeed a great pleasure to note that our Department is publishing the newsletter on half yearly basis on every aspect of activities and events held during the semester. The newsletter aims to bring into view various proceedings, functions, events, news, accomplishments and happenings in the Department.

Newsletter acts as a communication channel among the alumni, faculty, students and experts in Electronics and Communication Engineering.

We are very much grateful to the Management, Principal and Vice Principal for their continuous encouragement, inspiration and support extended for the release of this

*"NEWSLETTER"*

***Editorial Board Members:***

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2. Anjali Mishra (4th Sem)

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*Vice-Principal Gandhinagar Bhopal*

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# Vision Mission & PEOs of Electronics and Communications Department

## *Vision:*

To become one of the leading institutes in the field of electronics and communication engineering to produce competent and committed technocrats who strive continuously in the pursuit of professional excellence for the betterment of society.

## *Mission Statements:*

To impart learning skills through rigorous academic and technological intervention to meet the global challenges in the field of Electronics and Communication Engineering.

To inculcate the significance of human values and professional skills to serve the society.

To develop professional skills through co-curricular, extra-curricular, and industry interaction activities.

## *Program Educational Objectives:*

**PEO 1:** To develop an ability among students to understand the concept of Electronics and Communication Engineering that will facilitate the understanding of new technology that prepares them for a professional career and higher studies.

**PEO 2:** To inculcate professional and ethical skills, entrepreneurial thinking, and an ability to relate engineering issues with social issues.

**PEO 3:** To give exposure to emerging edge technologies, adequate training, and opportunities to work as a team on multidisciplinary projects with effective communication skills and leadership qualities.

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# SEMESTER BREAK TRAINING

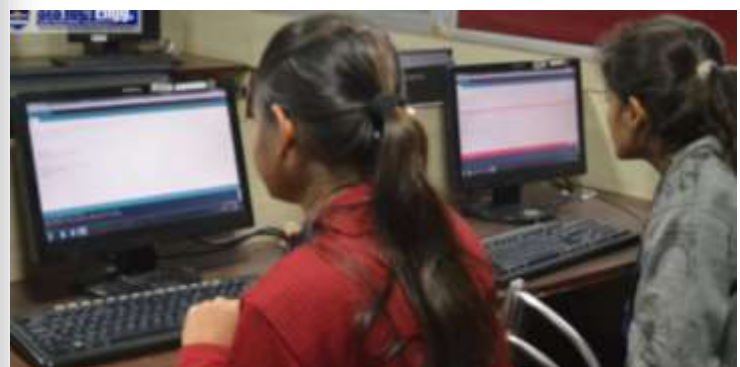
## ELECTRONIC CIRCUIT DESIGN AND TESTING

**THE OBJECTIVE OF TRAINING IS TO DEVELOP SKILLS NEEDED IN AN ELECTRONICS ENGINEER.**

SISTec Department of Electronics and Communication Engineering conducted training on "Electronic Circuit Design and Testing" for first year students of ECE from 1 Jan to 3 Jan 2020.

On day two Prof. Sharad Shrivastava, started session with introduction to basic electronic components, their types, color coding, their series and parallel connections, in the other half of the day, Prof. Siddharth Singh along with Prof. Kavita Kamerikar took hand-on session on analog and digital circuits.

On day three Prof. Rahul Shrivastava exposed students to embedded system, its components and characteristics. next students performed many hand-on exercises on an #Arduino board, later the students learned how to read sensor and display the information to a #smart\_phone using #bluetooth. At the end of the day, Prof. Shrivastava demonstrated data uploading to #ThingSpeak cloud platform.



# INDUSTRIAL VISIT

## MSME(Micro, Small and Medium Enterprises), Bhopal.

SISTec Department of Electronics and Communication Engineering organised industrial visit for their second year and third year students to MSME(Micro, Small and Medium Enterprises), Bhopal. The visit started with the session of Ms. Shashi Karautiya, she discussed about the importance of embedded systems, PLC and SCADA in an industrial environment.

In the second session hand-on session was conducted by Mr. Ankur on ARM processors. Also session were conducted on PLC and SCADA over TIA portal.

The objective of visit is to develop understanding on opportunities provided by MSME, and use of PLC and SCADA in an industrial environment.



# PLACEMENT

## FLDec Systems

**SISTec**  
Gandhi Nagar & Ratibad, Bhopal  
Engg. | Pharm. | MBA  
SAGAR INSTITUTE OF SCIENCE & TECHNOLOGY

*A gateway to placements...*

# Congratulations

TO

**Gagan Parihar**  
EC - SISTec, Ratibad

**Rama Malviya**  
EC - SISTec, Gandhi Nagar

**Ashish Pandey**  
EC - SISTec, Gandhi Nagar

**Prabhati Mohanta**  
EC - SISTec, Ratibad

**Deepesh Jain**  
EE - SISTec, Ratibad

**Shashank S. Pandey**  
EX - SISTec, Gandhi Nagar

**FLDec**  
SYSTEMS

BATCH 2016 - 2020  
FOR GETTING SELECTED IN  
FLDec Systems  
Profile : VLSI Engineer

www.sistec.ac.in

Congratulations to Ms. Rama Malviya and Mr. Ashish Pandey for getting placed in FLDec Systems at the profile of VLSI Engineer. Best wishes to all your future endeavors.

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# PLACEMENT

## TCS (NINJA DEVELOPER)

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 A Gateway to Placements...

**2020**  
 BENCHMARKING EXCELLENCE IN EDUCATION FOR 20 YEARS  
 20 YEARS OF THE DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

**Congratulations**  
 for getting placed in

**TATA**  
 TATA CONSULTANCY SERVICES

Profile: **Ninja Developer**  
 Package : **3.5 LPA**

 Vijay Raj Singh Sisodiya CSE - SISTec Gandhi Nagar	 Harshal Gekhe CSE - SISTec Gandhi Nagar	 Shivanshu Srivastava CSE - SISTec Gandhi Nagar	 Ayush Kurup CSE - SISTec Gandhi Nagar
 Manhan Jain CSE - SISTec Gandhi Nagar	 Archit Gupta CSE - SISTec Gandhi Nagar	 Bharat Kabra CSE - SISTec Gandhi Nagar	 Shayan Ahmad CSE - SISTec Gandhi Nagar
 Kapil Nema CSE - SISTec Gandhi Nagar	 Manya Vinayakarma CSE - SISTec Gandhi Nagar	 Nishant Sisodia CSE - SISTec Gandhi Nagar	 Akash Singh EC - SISTec Gandhinagar
 Sourabh Goid CSE - SISTec Gandhi Nagar	 Akshat Tamrakar CSE - SISTec Gandhinagar	 Kavert Gupta CSE - SISTec Gandhi Nagar	 Amaan Anwar CSE - SISTec Gandhi Nagar
 Diksha Dokrimane CSE - SISTec Gandhinagar	 Firoza Bee CSE - SISTec Gandhinagar	<i>Best wishes for your bright future...</i>	

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Congratulations to all for getting placed in TCS as NINJA DEVELOPER at 3.5 LPA.

#BestWishes for all your future endeavors.

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# PLACEMENT

## LEAP LEARNER (CODING TRAINEE)



SISTecECE congratulates Shweta Malviya, for getting placed as Coding Trainee (Research - Robotics) in Leap Learner.

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# PLACEMENT

## PLASTECH SOLUTIONS (GRADUATE ENGINEER TRAINEE)

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 20 YEARS OF THE SAGAR GROUP OF INSTITUTIONS

**Congratulations**  
 for getting placed in

**PLASTECH SOLUTIONS** | Package : **1.8 LPA**

**PROFILE : Graduate Engineer Trainee**

 Padam Singh ME - SISTec Gandhi Nagar	 Rajendra Singh ME - SISTec Ratibad	 Harendra Kumar Yadav ME - SISTec Ratibad	 Md Afroz Alam ME - SISTec Ratibad
 Prince Singh ME - SISTec Ratibad	 Vishal Namdev ME - SISTec Ratibad	 Shubham Ranjan ME - SISTec Gandhi Nagar	 Sachin Kumar Yadav ME - SISTec Ratibad
 Ritish Kumar Singh ME - SISTec Ratibad	 Ashish Pandey EC - SISTec Gandhi Nagar	 Naam Khan EC - SISTec Ratibad	 Pratik Shekhar EC - SISTec Gandhi Nagar

**PROFILE : IoT Engineer**

 Prabhati Mohanta EC - SISTec Ratibad	 Himanshu Gupta EC - SISTec Ratibad
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**PROFILE : AI Trainee**

 Sunil Kumar Sharma CM - SISTec Ratibad	 Ramao Dubey CM - SISTec Ratibad
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*Best wishes for your bright future...*

B. Tech | M.Tech | B. Pharma | M. Pharma | MBA  
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Congratulations to Mr. Prateek Shekhar and Mr. Ashish Pandey for getting placed in Plastech Solutions, Pune at the profile of Graduate Engineer Trainee.

#BestWishes to all your future endeavors.

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# EXPERT TALK

## Cyber Security and Ethical Hacking

SISTec Department of Electronics and Communication Engineering organized an expert talk on "Cyber Security and Ethical Hacking" for second year and third year students of #SISTecECE and SISTec Department of Computer Science & Engineering on 3 Feb 2020.

The talk was delivered by Mr. Ajinkya Lohakare, CTO of Social-Spectra & Dreamssofts, He is very young energetic and enthusiastic person, has certifications in Ethical Hacking, Certified Hacking Forensic Investigator, Google Certified Application Developer, Block Chain & Crypto currency Expert.



He discussed about various methods and tools available for ethical hacking and also the methods, tools and practices to be followed over internet for safe transactions and data security. He also motivated students to understand the underlying processes and architecture of various internet products.

#Internet #CyberSecurity  
#EthicalHacking #Social\_Spectra



# INDUSTRIAL VISIT

## Telecommunication Department, Western Central Railways, Bhopal (M.P)

Industrial Visits offer a great source to gain practical knowledge. Students can observe and learn as to how theoretical concepts are put into action, thereby adding their practical learning. Students are exposed to real working environment and are shown how things are done in an organisation.

With this view in mind SISTecECE organized industrial visit to Signal and Telecommunication Department, Western Central Railways, Bhopal (M.P)



Mr. Manish Choudhary, Senior Section Engineer, S&T Bhopal Division along with his team provided a visit to the Railway Telephone Exchange, OFC Station, Passenger Reservation System and the Integrated Security System at Bhopal Junction Railway station and explained the work flow in detail.

# PLACEMENT

## BIG BASKET (QUALITY ANALYST)



*A Gateway  
to Placements...*

*Congratulations*  
for getting placed in



Profile:  
**Quality Analyst**  
Package : **3.2 LPA**



*Best wishes for your bright future*

B. Tech | M.Tech | B. Pharma | M. Pharma | MBA  
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# INDUSTRIAL VISIT

## Telecommunication Department, Western Central Railways, Bhopal (M.P)



SISTec Department of Mechanical Engineering organized 2 Days Nirmaan 2020 on 28th Jan 20 - 29th Jan 20. This year Nirmaan 2020 has brought an Amazing Electrical Vehicle Startup Conclave, sponsored by RGPV under TEQIP-III, in association with ARAI (Automotive Research Association of India).



A team from SISTec Department of Electronics and Communication Engineering, second year, won a cash prize of Rs. 10000/- under best innovation category. The idea is #DriverLess #car, many sensors are deployed with a microcontroller unit in this car so that the car can reach the specified location safely.

Team members are:

Richa Soni, Ashutosh Dubey, Rahul Kumar, Anjali Mishra, Kareena Verma, Deepak k Shah, Vikas Mewada and Aman Mansoori. The team is guided by Dr. Paresh Rawat. Many facilities are to be added, #SISTecECE wishes them for their future endeavours !!